



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

|                      |                        |   |                        |               |
|----------------------|------------------------|---|------------------------|---------------|
| <b>APPLICANT(S):</b> | CHRIS CHUTER           | § | <b>ATTORNEY DOCKET</b> | HESI.105897 / |
|                      |                        | § | <b>NUMBER:</b>         | 2003-IP-09595 |
|                      |                        | § |                        |               |
| <b>SERIAL</b>        |                        | § | <b>ART UNIT:</b>       |               |
| <b>NUMBER:</b>       | 10/628,781             | § |                        |               |
|                      |                        | § |                        |               |
| <b>FILING DATE:</b>  | JULY 28, 2003          | § |                        |               |
|                      |                        | § |                        |               |
| <b>FOR:</b>          | SYSTEM AND METHOD FOR  | § | <b>EXAMINER:</b>       |               |
|                      | REAL-TIME CO-RENDERING | § |                        |               |
|                      | OF MULTIPLE ATTRIBUTES | § |                        |               |

**INFORMATION DISCLOSURE STATEMENT**

Commissioner of Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Listed on accompanying Form PTO-1449 are documents that may be considered material to the examination of this application, in compliance with the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98. A copy of each of these documents is provided.

Applicant reserves the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

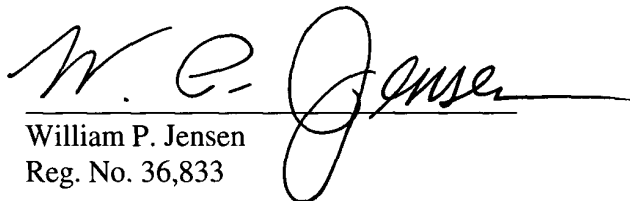
The filing of this information disclosure statement shall not be construed to be an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in §1.56(b).

This statement should not be construed as a representation that a search has been made, or that information more material to the examination of the present patent application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith. It is further understood that the Examiner will consider information that had been cited by or submitted to the U.S. Patent and Trademark Office in a prior application relied on under 35 U.S.C. & 120.1138 OG 37, 38 (May 19, 1992).

It is respectfully requested that the Examiner initial and return a copy of the enclosed PTO-1449, and to indicate in the official file wrapper of this patent application that the documents have been considered.

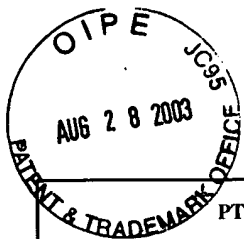
Applicant does not believe that a fee is due with this submission, however, the Commissioner is hereby authorized to charge any fee deficiency, or credit any overpayment to our Deposit Account No.19-2112.

Respectfully submitted,

  
William P. Jensen  
Reg. No. 36,833

Date: August 25, 2003

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| <b>PTO-1449 (Modified)</b><br><br><b>U.S. DEPARTMENT OF COMMERCE</b><br><b>PATENT AND TRADEMARK OFFICE</b><br><br><b>INFORMATION DISCLOSURE STATEMENT</b><br><b>BY APPLICANT</b> | <b>Attorney</b> 2003-IP-09595 /<br><b>Docket No.:</b> HESI.105897 | <b>Serial</b> 10/628,781<br><b>Number:</b> |
|  | <b>Applicant:</b> Chris Chuter                                    |  |
|  | <b>Filing Date:</b> July 28, 2003                                 | <b>Group:</b>                              |

**U.S. PATENT DOCUMENTS**

| EXAMINER<br>INITIAL | PATENT NUMBER                            | ISSUE<br>DATE | INVENTOR           | CLASS | SUBCLASS | FILING DATE |
|---------------------|--|---------------|--------------------|-------|----------|-------------|
|                     | Pub. No.<br>2002/0172401<br>(09/936,780) | —             | Lees, Jack, et al. | 382   | 109      | 9/17/01     |
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**FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION**

| EXAMINER<br>INITIAL | DOCUMENT<br>NUMBER | PUBLICATION<br>DATE | COUNTRY OR<br>PATENT OFFICE | CLASS | SUBCLASS | TRANSLATION |    |
|---------------------|--------------------|---------------------|-----------------------------|-------|----------|-------------|----|
|                     |                    |                     |                             |       |          | YES         | NO |
|                     |                    |                     |                             |       |          |             |    |
|                     |                    |                     |                             |       |          |             |    |

| EXAMINER<br>INITIAL   | OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place Of Publication)  |
|---|--|
|   | Mark J. Kilgard, "A Practical and Robust Bump-mapping Technique for Today's GPUs," July 5, 2000, pp. 1-39, NVIDIA Corporation, Santa Clara, CA |
|   | Jack Lees, "Constructing Faults from Seed Picks by Voxel Tracking," March 1999, pp. 338, 340, <i>The Leading Edge</i> .                        |
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| <b>EXAMINER</b>   | <b>DATE CONSIDERED</b>   |
| <b>EXAMINER:</b> Initial citation if reference was considered. Draw line through citation if not in conformance to MPEP 609 and not considered. Include copy of this form with next communication to applicant. |  |